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K.K. Parhi, VLSI Digital  
Signal Processing  
Systems: Design and  
Implementation, John  
Wiley, 1999

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DSP systems can be



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realized using programmable processors or custom designed hardware circuits fabricated using very-large-scale-integrated (VLSI) circuit technology. The goal of digital design is to maximize the performance while keeping the cost down.

## **1.1 Introduction - VLSI Digital Signal Processing Systems**

...

*Page 9/28*

Adaptive filters are often realized either as a set of program instructions running on an arithmetical processing device such as a Microprocessor or Digital Signal Processing (DSP) chip, or as a set of logic operations implemented in a field programmable gate array (FPGA) or in Very Large Scale Integrated Circuit (VLSI).

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Processing (DSP) and  
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There are four fundamental rounding modes whose matlab function names are: 1) round( $\cdot$ ): towards nearest integer • Generally the best rounding algorithm 2) fix( $\cdot$ ): truncates towards zero 3) floor( $\cdot$ ): rounds towards negative infinity

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Processing  
(1995), Kluwer  
Academic Publishers,  
Boston. Keshab K.  
Parhi and David G.  
Messerschmitt, Pipeline  
Interleaving and  
Parallelism in  
Recursive Digital Filters  
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MOS transistor mismatch is revisited in the context of subthreshold operation and VLSI systems. We report experimental measurements from large transistor arrays with device sizes typical for digital and analog VLSI systems (areas between 9 and  $400\mu\text{m}^2$ ). These are fabricated at different production qualified facilities in 40-nm gate oxide, n-well and p-well,

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## **Saurin Shah - Research Assistant - Digital/Analog VLSI Implementation**

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